

*Jmope*

Attorney's Docket No.: 10559-595001/P12879

ad ad ✓

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al. Art Unit: 2825
Serial No.: 09/942,102 Examiner: Annette M.
Filed : August 29, 2001 Thompson
Assignee : Intel Corporation
Title : GENERATING A LOGIC DESIGN

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

Copies of the U.S. patents and publications are not being provided per the waiver posted on the PTO website.

Kindly accept this Information Disclosure Statement under Rule 97(c) (2). The rule 17(p) certification fee of \$180 is enclosed.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a) (3).

03/30/2004 FFANAEIA 00000025 09942102

01 FT:1806

180.00 0P

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

March 25, 2004

Date of Deposit

Signature

Jennifer H. Payne

Typed or Printed Name of Person Signing Certificate

The PTO did not receive the following listed item(s) Reference with *

Applicant : William R. Wheeler et al.
Serial No.: 09/942,102
Filed : August 29, 2001

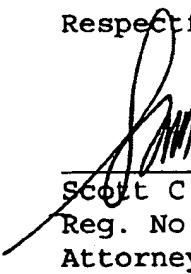
Attorney's Docket No.:10559-595001

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 3/24/04



Scott C. Harris
Reg. No. 32,030
Attorney for Intel Corporation

Fish & Richardson P.C.
USPTO Customer No. 20985
12390 El Camino Real
San Diego, CA 92130
Telephone: (858) 678-5070
Facsimile: (858) 678-5099

10379135.doc

Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-595001	Application No. 09/942,102
Information Disclosure Statement by Applicant <small>(Use several sheets if necessary)</small> <small>(37 CFR §1.98(b))</small>		Applicant	William R. Wheeler et al.	
		Filing Date	August 29, 2001	

J.C.
MAR 29 2004
U.S. PATENT & TRADEMARK OFFICE

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	RE 38,059	04/2003	Yano, et al.			
	AB	4,703,435	10/1987	Darringer, et al.			
	AC	4,970,664	11/1990	Kaiser, et al.			
	AD	5,212,650	05/1993	Hooper, et al.			
	AE	5,267,175	11/1993	Hooper			
	AF	5,287,289	02/1994	Kageyama, et al.			
	AG	5,297,053	03/1994	Pease, et al.			
	AH	5,475,605	12/1995	Lin			
	AI	5,493,507	02/1996	Shinde, et al.			
	AJ	5,568,397	10/1996	Yamashita, et al.			
	AK	5,598,347	01/1997	Iwasaki			
	AL	5,603,015	02/1997	Kurosawa, et al.			
	AM	5,604,894	02/1997	Pickens, et al.			
	AN	5,663,662	09/1997	Kurosawa			
	AO	5,685,006	11/1997	Shiraishi			
	AP	5,694,579	12/1997	Razdan, et al.			
	AQ	5,706,476	01/1998	Giramma			
	AR	5,724,250	03/1998	Kerzman, et al.			
	AS	5,757,655	05/1998	Shih, et al.			
	AT	5,809,283	09/1998	Vaidyanathan, et al.			
	AU	5,841,663	11/1998	Sharma, et al.			
	AV	5,892,682	04/1999	Hasley, et al.			
	AW	5,903,469	05/1999	Ho			
	AX	5,937,190	08/1999	Gregory, et al.			
	AY	5,974,242	10/1999	Damarla, et al.			
	AZ	6,077,304	06/2000	Kasuya			
	AAA	6,208,954	03/2001	Houtchens			

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 <i>(Amendment)</i> MAR 29 2004 (37 CFR §1.98(e))		U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 10559-595001	Application No. 09/942,102
Information Disclosure Statement by Applicant <i>(Use several sheets if necessary)</i>		Applicant William R. Wheeler et al.			
		Filing Date August 29, 2001	Group Art Unit 2825		

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,216,256	04/2001	Inoue, et al.			
	ACC	6,234,658	05/2001	Houldsworth			
	ADD	6,275,973	08/2001	Wein			
	AEE	6,292,931	09/2001	Dupenloup			
	AFF	6,327,693	12/2001	Cheng, et al.			
	AGG	6,353,806	03/2002	Gehlot			
	AHH	6,353,915	03/2002	Deal, et al.			
	AII	6,381,563	04/2002	O'Riordan, et al.			
	AJJ	6,381,565	04/2002	Nakamura			
	AKK	6,421,816	07/2002	Ishikura			
	ALL	6,438,729	08/2002	Ho			
	AMM	6,438,731	08/2002	Segal			
	ANN	6,440,780	08/2002	Kimura, et al.			
	AOO	6,473,885	10/2002	Wallace			
	APP	6,477,688	11/2002	Wallace			
	AQQ	6,490,545	12/2002	Peng			
	ARR	6,519,742	02/2003	Falk			
	ASS	6,523,156	02/2003	Cirit			
	ATT	6,539,536	03/2003	Singh et al.			
	AUU	6,546,528	04/2003	Sasaki, et al.			
	AVV	6,574,787	06/2003	Anderson			
	AWW	6,591,407	07/2003	Kaufman, et al.			
	AXX	2002/0038447	03/2002	Kim, et al.			
	AYY	2002/0059054	05/2002	Bade, et al.			
	AZZ	2002/0112221	08/2002	Ferreri, et al.			
	AAAA	2002/0138244	09/2002	Meyer			
	ABBB	2003/0004699	01/2003	Choi, et al.			

Examiner Signature	Date Considered
--------------------	-----------------

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 Modified OTPE Information Disclosure Statement by Applicant MAR 29 2004 (37 CFR §1.98(e))		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-595001	Application No. 09/942,102
		Applicant William R. Wheeler et al.		
		Filing Date August 29, 2001	Group Art Unit 2825	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ACCC	2003/0036871	02/2003	Fuller, et al.			
	ADDD	2003/0177455	09/2003	Kaufman, et al.			

Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes No
	AEEE	0 404 482	12/1990	EPO			
	AFFF	0 720 233	07/1996	EPO			
*	AGGG	0 901 088	03/1999	EPO			
	AHHH	58-060559	04/1983	Japan			
	AIII	03-225523	10/1991	Japan			
	AJJJ	07-049890	02/1995	Japan			
	AKKK	08-314892	11/1996	Japan			
	ALLL	2001-068994	03/2001	Japan			
	AMMM	WO 98/55879	12/1998	WIPO			
*	ANNN	WO 99/39268	08/1999	WIPO			
*	AOOO	WO 00/65492	11/2000	WIPO			

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	APPB	Gassenfeit, E. H., "Control System Design Realization via VHDL-A: Requirements", Proceedings of the 1996 IEEE International Symposium on Computer-Aided Control System Design, September 15, 1996, pp. 282-285.
	AQQQ	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, September 17, 2000, pp. 487-493.
	ARRR	Lahti, et al., "SADE: a Graphical Tool for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
	ASSS	Lin, et al., "A Goal Tree Based High-Level Test Planning System for DSP Real Number Models", 1998 Proceedings of International Test Conference, October 18, 1998, pp. 1000-1009.
	ATTT	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", <i>Electrical Design News</i> , 41(14):129-136 (1996).
	AUUU	NB84035598, "Binary Relational Schema to Model Structured LSI Design", IBM Technical Disclosure Bulletin, Vol. 26, No. 10B, March 1984, pp. 5598-5601.

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Mod. 01-04)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-595001	Application No. 09/942,102
Information Disclosure Statement by Applicant (use several sheets if necessary) <i>(37 CFR §1.98(b))</i>		Applicant William R. Wheeler et al.	
		Filing Date August 29, 2001	Group Art Unit 2825

Other Documents (Include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AVVV	NN7807629, "Functional Oriented Symbolic Macromodeling Algorithm", IBM Technical Disclosure Bulletin, Vol. 21, No. 2, July 1978, pp. 629-631.
	AWWW	NN8006341, "Macro Physical-To-Logical Checking LSI Chip Design", IBM Technical Disclosure Bulletin, Vol. 23, No. 1, June 1980, pp. 341-345.
	AXXX	NN9407481, "Functional Modeling using object Collaboration Diagram", IBM Technical Disclosure Bulletin, Vol. 37, No. 7, July 1994, pp. 481-486.
	AYYY	Parlakbilek, et al., "A Multiple-Strength Multiple-Delay Compiled-Code Logic Simulator", <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 12(12):1937-1946 (1993).
	AZZZ	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", IEEE International Conference on Systems Engineering, August 9, 1990, pp. 474-477.

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	